

Applicant : Philip Brown et al.
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REMARKS

Attached is a marked-up version of the changes being made by the current amendment.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 9/16/02



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Version with markings to show changes made

In the specification:

Paragraph beginning at page 5, line 33 has been amended as follows:

-- Various aspects of a scalable switching system and methods of using the same are described in co-assigned (1) U.S. Patent Application No. 09/768, 528, entitled "Scalable Switching System and Method," filed on January 23, 2001, (2) U.S. Patent Application No. 09/779,414, entitled "Intelligent Flow Control and Input/Output Buffer Distribution for Removing Head-of-Line-Blocking," filed on February 6, 2001, (3) U.S. Patent Application No. 09/956,612, entitled "Multiple Schedulers in a Scalable [Scheduling]Switching System[,]", [(Attorney Docket No. M-11517)], filed on September 18, 2001 and (4) U.S. Patent Application No. 09/956,338, entitled "Virtual Output Queues in a Scalable [Scheduling]Switching System[,]", [(Attorney Docket No. M-11587)], filed on September 18, 2001, which are all hereby incorporated by reference in their entireties.--